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zPTO/SB/21 (09-04)

**TRANSMITTAL
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	10/691,757 703 8870
Filing Date	October 22, 2003
First Named Inventor	Suzuki, Hiromasa
Art Unit	2651
Examiner Name	Varsha A. Kapadia
Attorney Docket Number	16869Q-093100US

ENCLOSURES (Check all that apply)

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| <input type="checkbox"/> Fee Transmittal Form
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Remarks

The Commissioner is authorized to charge any additional fees to Deposit Account 20-1430.

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of Correction****SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm Name	Townsend and Townsend and Crew LLP		
Signature			
Printed name	Chun-Pok Leung		
Date	06/01/06	Reg. No.	41,405

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Certificate of Correction Branch, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,038,870 B2
ISSUED : May 2, 2006
INVENTOR(S) : Hiroaki Suzuki et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 13, Line 56

should read as -- a write circuit configured to generate the write current to be --, not "a write circuit configured to generate the current to be"

Column 13, Line 58

should read as -- voltage and a supplied negative voltage;--, not "voltage and a supplied negative voltage"

Column 14, Line 8

should read as -- setting by a controller the magnitude of a negative--, not "setting by a controller the magnitude of the negative"

Column 15, Line 2

should read as -- verter for converting the voltage in accordance with the value--, not "verter for converting the voltage in accordance withthe value"

MAILING ADDRESS OF SENDER: Chun-Pok Leung, Reg. No. 41,405
TOWNSEND and TOWNSEND and CREW LLP
Two Embarcadero Center, 8th Floor
San Francisco, CA 94111-3834

PATENT NO. 7,038,870 B2

No. of additional copies: 10



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